

INVENTOR INFORMATION

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APPLICATION INFORMATION

Title Line One:: METHOD FOR MANUFACTURING MEMORY WITH HIG
Title Line Two:: H CONDUCTIVITY BITLINE AND SHALLOW TRENC
Title Line Three:: H ISOLATION INTEGRATION
Total Drawing Sheets:: 5
Formal Drawings?: Yes
Application Type:: Utility
Docket Number:: G0807
Secrecy Order in Parent Appl.?: No

REPRESENTATIVE INFORMATION

Registration Number One:: 27449
Registration Number Two:: 36428
Registration Number Three:: 34552
Registration Number Four:: 37797
Registration Number Five:: 33667
Registration Number Six:: 33491
Registration Number Seven:: 44168
Registration Number Eight:: 36654
Registration Number Nine:: 27688
Registration Number Ten:: 32638

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